# TFA: A Tunable Finite Automaton for Pattern Matching in Network Intrusion Detection Systems

Yang Xu<sup>†</sup>, Junchen Jiang<sup>§</sup>, Rihua Wei<sup>‡</sup>, Yang Song<sup> $\sharp$ </sup> and H. Jonathan Chao<sup>†</sup>

<sup>†</sup>New York University, Polytechnic School of Engineering, USA

<sup>§</sup>Carnegie Mellon University, USA

<sup>‡</sup>Tensorcom Inc., USA

<sup>#</sup>University of Utah, USA

Abstract-Deterministic Finite Automatons (DFAs) and Nondeterministic Finite Automatons (NFAs) are two typical automatons used in the Network Intrusion Detection System (NIDS). Although they both perform regular expression matching, they have quite different performance and memory usage properties. DFAs provide fast and deterministic matching performance but suffer from the well-known state explosion problem. NFAs are compact, but their matching performance is unpredictable and with no worst case guarantee. In this paper, we propose a new automaton representation of regular expressions, called Tunable Finite Automaton (TFA), to deal with the DFAs' state explosion problem and the NFAs' unpredictable performance problem. Different from a DFA, which has only one active state, a TFA allows multiple concurrent active states. Thus, the total number of states required by the TFA to track the matching status is much smaller than that required by the DFA. Different from an NFA, a TFA guarantees that the number of concurrent active states is bounded by a bound factor b that can be tuned during the construction of the TFA according to the needs of the application for speed and storage. Simulation results based on regular expression rule sets from Snort and Bro show that with only two concurrent active states, a TFA can achieve significant reductions in the number of states and memory usage, e.g., a 98% reduction in the number of states and a 95% reduction in memory space.

Index Terms—Regular Expression, Finite Automaton, Pattern Matching, State Explosion

#### I. INTRODUCTION

Deep Packet Inspection (DPI) is a crucial technique in today's Network Intrusion Detection System (NIDS), where it compares incoming packets byte-by-byte against patterns stored in a database to identify specific viruses, attacks and protocols. Early DPI methods rely on exact string matching [1] [2] [3] [4] for attack detection, whereas recent DPI methods use regular expression matching [5] [6] [7] [8] [9] because the latter provides better flexibility in representing the ever-evolving attacks [10]. Regular expression matching has been widely used in many NIDSes such as Snort [11], Bro [12], and several network security appliances from Cisco systems [13] and has become the de facto standard for content inspection.

Despite its flexible attack representation, regular expression matching introduces significant computational and storage challenges. Deterministic Finite Automatons (DFAs) and Nondeterministic Finite Automatons (NFAs) are two typical representations of regular expressions. Given a set of regular expressions, we can easily construct the corresponding NFA, from which the DFA can be further constructed using subset construction scheme [14]. DFAs and NFAs have quite different performance and memory usage characteristics. A DFA has at most one active state during the entire matching and, therefore, requires only one state traversal for each character processing, resulting in a deterministic memory bandwidth requirement. The main problem of using a DFA to represent regular expressions is the DFA's severe state explosion problem [5], which often leads to a prohibitively large memory requirement. In contrast, an NFA represents regular expressions with much less memory storage. However, this memory reduction comes with the price of a high and unpredictable memory bandwidth requirement. This is because the number of concurrent active states in an NFA is unpredictable during the matching. Processing a single character in a packet with an NFA may induce a large number of state traversals, which translate into a large number of memory accesses and limit the matching speed.

1

Recently, many research works have been proposed in literature pursuing a tradeoff between the computational complexity and storage complexity for the regular expression matching [5] [6] [7] [8] [10] [15] [16]. Among these proposed solutions, some [8] [10] have a motivation similar to ours, i.e., to design a hybrid finite automaton fitting between DFAs and NFAs. These automatons, though compact and fast when processing common traffic, suffer from degraded performance in the worst cases due to the unbounded number of active states during the matching processing. This weakness can potentially be exploited by attackers to construct a worst-case traffic that can slow down the NIDS and cause malicious traffic to escape from the inspection.

In fact, the design of a finite automaton with a small (larger than one) but bounded number of active states remains an open and challenging problem. In this paper, we propose Tunable Finite Automaton (TFA), a new automaton representation, for regular expression matching to deal with the DFAs' state explosion problem and NFAs' unpredictable performance problem. The main idea of TFA is to use a few TFA states to remember the matching status traditionally tracked by a single DFA state. As a result, the number of TFA states required to represent the information stored on the counterpart DFA is much smaller than that of DFA states. Unlike an NFA, a TFA has the number of concurrent active states strictly bounded by a bound factor b, which is a parameter that can be tuned during the construction of the TFA according to the needs for speed and storage.

This paper is an extension of our previous technical report [17] and the invented automata have been deployed by [18] in their work of supporting massive pattern queries in a resource constrained database system. Our main contributions in this paper are summarized below.

(1) We introduce TFA, which to the best of our knowledge, is the first finite automaton model with a clear and tunable bound on the number of concurrent active states (more than one) *independent of the number and patterns of regular expressions.* TFA is a general finite automaton model, which covers DFA and NFA as two special cases. It becomes DFA when the bound factor b is set to 1 and NFA when b is set to infinite. In addition, a TFA can be equivalently constructed from any NFAs and therefore supports all regular expressions.

(2) A mathematical model is built to analyze the set split problem (SSP), the most critical step in the TFA construction. We prove that the SSP problem is NP-hard and propose a heuristic algorithm to approximately solve it.

(3) We develop a novel state encoding scheme to facilitate the implementation of a TFA. With the state encoding, a TFA can be stored in a compacted memory, and the run-time overheads of TFA operations are significantly reduced.

(4) The proposed TFA is evaluated using regular expression sets from Snort and Bro. Simulation results show that a TFA can achieve significant reductions in the number of states and memory usage even with the simplest TFA (only two concurrent active states).

The rest of the paper is organized as follows. Section II reviews the related work. Section III presents the motivation and main idea of the paper. Section IV explains the technical details of the TFA, including its mathematical model, construction scheme, and operation procedure. In section V, we formalize an important problem in the TFA design: set split, and propose a heuristic algorithm to solve it. Section VI presents the state encoding scheme. Section VII presents the simulation results. Finally, section VIII concludes the paper.

#### **II. RELATED WORK**

Most of the research today in regular expression matching focuses on reducing the memory usage of DFAs and can be classified into the following categories:

#### (1) Transition reduction

Schemes in this category reduce the memory usage of a DFA by eliminating redundant transitions. The D<sup>2</sup>FA [6] proposed by Kumar et al. is a representative method in this category. It eliminates redundant transitions in a DFA by introducing default transitions, and saves memory usage at the cost of increasing the memory access times for each input character. After the D<sup>2</sup>FA, many other schemes, such as the CD<sup>2</sup>FA [15] and [19] were proposed to improve the D<sup>2</sup>FA's worst-case run-time performance and construction complexity.

# (2) State reduction

Schemes in this category reduce the memory usage of a DFA by alleviating its state explosion. Due to the fact that many regular expressions interact with others, the composite DFA for multiple regular expressions could possibly be extremely large (i.e., state explosion). Yu et al. [5] and Jiang et al. [20] proposed DFA grouping schemes to combine regular expressions into multiple DFAs instead of one to eliminate the state explosion. The main idea of DFA grouping scheme is to divide the original set of regular expressions into several smaller subsets, and turn each subset into a different DFA. In order to mitigate the state explosion within each subset, the scheme tends to choose regular expressions with less interaction into the same subset. As the original regular expression set is broken into smaller subsets, the severity of state explosion is reduced. Therefore, its storage cost is much less than that of a single DFA solution. To scan an input stream, all DFAs have to run in parallel, and therefore its run time complexity depends on the number of divided subsets. The XFA [7] [21] uses auxiliary memory to achieve a significant memory reduction. Unfortunately, the creation of XFA involves a lot of manual work which is error-prone and inefficient and its performance is non-deterministic. In [22], Becchi proposed an algorithm to merge DFA states by introducing labels on their input and output transitions. In [23], Kumar et al. proposed history-based finite automatons to record history information in matching which capture one of the major reasons for DFA state explosion and reduce the memory cost. However, to record history will increase the worst case complexity and thus compromise scalability.

(3) Hybrid Finite Automaton

Schemes in this category aim at designing automatons fitted into the middle ground between NFAs and DFAs so that the strengths of both NFAs and DFAs can be obtained. Becchi et al. proposed a hybrid finite automaton called Hybrid-FA [8], which consists of a head DFA and multiple tail-NFAs/tail-DFAs. When constructing a Hybrid-FA, any nodes that would contribute to state explosion retain an NFA encoding, while the rest are transformed into DFA nodes [8]. Consider a set of four regular expressions: ab.\*cd, cefc, cad and efb, as an example. The construction of the Hybrid-FA will first check which part(s) of the four regular expressions would possibly cause state explosion. In this case, only the dot-star term of the first regular expression will possibly lead to state explosion [8]. Therefore, Hybrid-FA will combine the partial-expression of the first regular expression before the dot-star term (i.e., ab) and the other three regular expressions together and turn them into a (head) DFA, which will not cause any state explosion because all of them are just pure strings. The remaining part of the first regular expression (i.e., .\*cd) will be treated separately and turned into a (tail) FA. If there are multiple regular expressions with those repetition (i.e., dot-star) patterns or counting constraints (i.e., the repetition of a sub-pattern for a given number of times), then each of them will lead to a separate tail FA to avoid the state explosion. There is a border state between the head FA and each tail FA, which belongs to both the head FA and the tail FA and behaves as an exit of the head DFA and the start state of the tail FA. The tail FA can be implemented by either a DFA or an NFA. When scanning an input stream, there is one and exactly one active state in the head DFA, while each tail FA can have one or multiple active

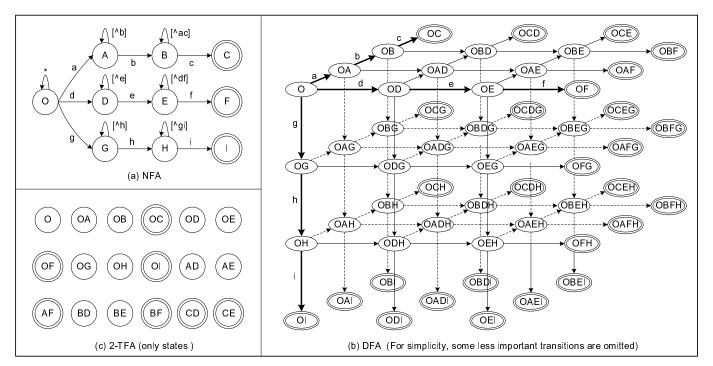


Fig. 1. Example of an NFA, a DFA and a 2-TFA associated with three regular expressions: .\*a.\*b[^a]\*c, .\*d.\*e[^d]\*f and .\*g.\*h[^g]\*i.

states if its border state is triggered by more than one time. In [8], it is proved that if a tail FA is implemented by DFA there will be only one active state in each tail DFA. Compared with a DFA, a Hybrid-FA requires much less storage because many parts of it are compiled separately and multiple active states are allowed to run in parallel (one in the head DFA and the others spread in tail FAs). However, as each dot-star and counting constraint will generate one tail FA (although some of them can be combined into the same tail FA or into the head DFA, at the expense of possible state explosion) and the head DFA and each tail FA can each have one active state at the same time in the worst cases, it is hard to guarantee an upper bound for the number of concurrently activated states in the Hybrid-FA. Lazy DFA [10] is another automaton used to leverage the advantages of both NFAs and DFAs. Its main function is to store only frequently used DFA states in memory, while leaving others in NFA representation. In case an uncommon DFA state is required, lazy DFA has to be extended at run-time from the NFA. So it is no surprise that this automaton is fast and memory-efficient in common cases, but in the worst case, the whole DFA needs to be expanded, making it vulnerable to malicious traffic. SFA (Semi-deterministic Finite Automata) proposed by Yang et al. [16] consists of a fixed number (p) of constituent DFAs (c-DFA) running in parallel and can provide a run-time performance much better than NFAs. NFA states without conflicts are preferred to be put into the same constituent DFA so that the state explosion can be mitigated. However, the value of p depends on the complexity of the regular expressions. In a complex NFA where conflicts exist between most of NFA state pairs, SFA can cause a relatively large number of c-DFAs and result in degraded performance.

# III. MOTIVATION

In this section, we first review the time-space tradeoff between an NFA and its counterpart DFA by way of example, and then demonstrate how a TFA combines both of their strengths. In Figure 1(a) and (b), we show an NFA and a DFA representing the same set of regular expressions, i.e.,  $\star a. \star b[^a] \star c, .\star d. \star e[^d] \star f, .\star g. \star h[^g] \star i$ , with the alphabet  $\Sigma = \{a, b, ..., i\}$ . We can see that although the NFA and DFA have the same functionality, the state number in the DFA (54) is 5.4 times that in the NFA (10).

Although the NFA requires much less memory, its memory bandwidth requirement is four times that of the DFA. This is because the NFA may have up to four concurrent active states while the DFA only has one. Consider an input string of "*adegf*". The initial active state combination <sup>1</sup> of NFA is {O}. The active state combinations of the NFA after the scanning of each of these characters are {O, A}, {O, A, D}, {O, A, E}, {O, A, E, G}, and {O, A, F, G}. We can see that after character "g" is read, there are four states: i.e., "O", "A", "E", and "G", active simultaneously in the NFA. Unlike the NFA, the DFA has only one state activated during the entire matching. Consider the same input string "*adegf*". The initial state of DFA is "O". The states visited by the DFA after each character is scanned are "OA", "OAD", "OAE", "OAEG", and "OAFG"<sup>2</sup>.

In Figure 2, we show the distribution of sizes of NFA active state combinations associated with two small regular expression rule sets from Snort. The integral of the distribution

<sup>&</sup>lt;sup>1</sup>In the paper, an NFA active state combination (or a combination of NFA active states) means one possible set of concurrently active NFA states.

 $<sup>^{2}</sup>$ In this paper, lowercase letters are used to denote input characters; single capital letters denote NFA states, while the strings of capital letters denote DFA and TFA states.

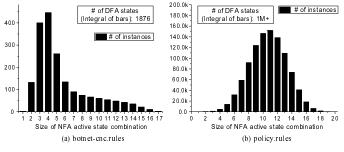


Fig. 2. Distribution of sizes of NFA active state combinations.

over the X-axis represents the size of the corresponding DFA. From the figure, we have three observations that (1) the NFA has many concurrent active states, even though its counterpart DFA is not that big (as in Figure 2 (a), the NFA has up to 17 states activated simultaneously, even though the corresponding DFA is very small with only 1876 states); (2) the integral of the distribution over the X-axis is much larger than the NFA size, resulting in the DFA state explosion (the sizes of NFA and DFA for Figure 2 (b) are 398 and 1M+, respectively. The rate of state explosion is more than 2.5K); (3) a larger state explosion rate implies a slower NFA and a bigger DFA.

We have seen the main reason for the DFA having far more states than the corresponding NFA is that the DFA needs one state for each NFA active state combination. If we want to reduce the DFA size (denoted by  $N_D$ ), one possible solution is to allow multiple automaton states (bounded by a given bound factor b) to represent each combination of NFA active states. In other words, we allow up to b active states in the new automaton, and name it Tunable Finite Automaton (TFA). For simplicity, we use b-TFA to denote a TFA with up to b active states.

To see the potential of a TFA, let  $N_T$  be the number of TFA states; the number of all possible statuses (denoted by P) that can be represented by at most b active states of the b-TFA is (normally,  $b \ll N_T/2$ )

$$P = \sum_{i=1}^{b} \begin{pmatrix} N_T \\ i \end{pmatrix} = O\left(N_T^{b}\right) \tag{1}$$

Thus, a TFA with  $N_T = O(\log_b (N_D))$  states can represent a DFA with  $N_D$  states.

# A. TFA States

The main idea of TFA is illustrated with an example. Suppose we want to design a 2-TFA based on the NFA in Figure 1(a). We first generate the corresponding DFA, which provides us with all valid combinations of NFA active states (Figure 1(b)). Then, we split each combination of NFA active states into two subsets, with the aim of minimizing the number of distinct subsets, and generate one 2-TFA state for each distinct subset. For instance, the NFA active state combination {O, A, D, G} can be split into {O, G} and {A, D}, and represented by two 2-TFA states "OG" and "AD".

Figure 1(c) shows a 2-TFA with only 18 states (only the 2-TFA states are given. The details of how it works will be provided shortly). It is easy to see that any valid combination of NFA active states can always be exactly covered by (at

most) two 2-TFA states. The 2-TFA (18 states, at most 2 active) achieves a significant reduction in the number of states compared to the corresponding DFA (54 states) and a significant reduction in the memory bandwidth requirement compared to the NFA (4 active states in the worst case).

# B. TFA Transitions

The most challenging part of designing a TFA is to connect TFA states with the proper transitions and let the TFA emulate the corresponding NFA and DFA. Recall that when an NFA scans an input string, the real-time matching status is tracked by its concurrent active states. To let a TFA emulate an NFA, we just need to guarantee that the active states of the NFA can be recovered from the active states of the TFA after each character is processed.

Consider the NFA in Figure 1(a) again and suppose that we have a 2-TFA as in Figure 1(c) that can emulate the NFA until time slot<sup>3</sup> t. At the end of time slot t, suppose there are two active states in the 2-TFA ("OD" and "OG") and the active state combination in the corresponding NFA is {O, D, G}. Now assume that the character read in time slot t + 1 is "a". It is easy to see that the active state combination of NFA at the end of time slot t + 1 would be {O, A, D, G}. The problem is how we could operate the 2-TFA to make it work exactly as the NFA does in time slot t + 1? If we run the two active states "OD" and "OG" separately, their next active states should be "OAD" and "OAG", respectively, which clearly are not in the state set given in Figure 1(c). Adding new state "OAD" and "OAG" into the state set of 2-TFA will cause a bigger automaton than we expected.

In this paper, we propose a novel way to operate a TFA. Rather than running TFA active states individually in each time slot to get the next TFA active states, we first recover the active state combination of the NFA by combining the results obtained by the individual TFA states. Then we access a table called *Set Split Table (SST)* to find out the next TFA active states whose combination is equal to this NFA active state combination. This way, no extra TFA states need to be added.

#### IV. TUNABLE FINITE AUTOMMATON(TFA)

A TFA can be generated from any NFA. In this section, we first give the formal definitions of NFAs, DFAs, and TFAs, and then present the procedure to generate the TFA based on an NFA. After that, we show how to operate the TFA to emulate the operation of the NFA.

#### A. Definition of TFAs

Remember that an NFA can be represented formally by a 5-tuple  $\langle Q_N, \Sigma, \delta_N, q_N, F_N \rangle$ , consisting of

- A finite set of NFA states  $Q_N$ ;
- A finite set of input symbols  $\Sigma$ ;
- A transition function  $\delta_N$ :  $Q_N \times \Sigma \mapsto P(Q_N)$ ;
- An initial state  $q_N$ ;

<sup>3</sup>One time slot is defined as the time period required to process a character. It is a constant (or variable) if the automaton has a deterministic (or non-deterministic) performance.

{O} &O NULL &0A NULL {A,O} {B,O} &OB NULL {A,D,O} &AD &0 {A,E,O} &OA &AE {A,E,G,O} &OG &AE {A,F,G,O} &OG &AF

(b) Set Split Table (SST)

• A set of accept states  $F_N \subseteq Q_N$ ;

where  $P(Q_N)$  denotes the power set of  $Q_N$ .

To be deterministic, a DFA consists of the similar 5-tuple  $\langle Q_D, \Sigma, \delta_D, q_D, F_D \rangle$  but with a transition function  $\delta_D$ :  $Q_D \times \Sigma \mapsto Q_D$  that transfers the current state to only one next state if any symbol is read.

Fig. 3. The architecture of a sample TFA (b = 2) associated with the NFA in Figure 1(a).

A *b*-TFA extends the 5-tuple definition of DFA/NFA, by introducing the set split function *SS*. Formally, a *b*-TFA is a 6-tuple  $\langle Q_T, \Sigma, \delta_T, I, F_T, SS \rangle$ , consisting of

- A finite set of TFA states  $Q_T$ ;
- A finite set of input symbols Σ;
- A transition function  $\delta_T : Q_T \times \Sigma \mapsto P(Q_N);$
- A set of initial states  $I \subseteq Q_T$ ,  $|I| \le b$ ;
- A set of accept states  $F_T \subseteq Q_T$ ;
- A set split function  $SS: Q_D \mapsto (Q_T)^b \bigcup \ldots \bigcup (Q_T)^1$ .

# B. Constructing A TFA

The implementation of a TFA, based on its definition, logically consists of two components: a TFA structure that implements  $Q_T, \Sigma, \delta_T, I, F_T$ ; and a *Set Split Table (SST)* that implements *SS*. Figure 3(a) and (b) show the TFA structure (with 18 isolated states) and SST table associated with the NFA in Figure 1(a). Each entry of the SST table corresponds to one combination of NFA active states (i.e., a DFA state) recording how to split the combination into multiple TFA states (the memory addresses of the TFA states are stored).

The main flowchart for generating a equivalent *b*-TFA from an NFA is given in Figure 4, which consists of four steps:

(1) Generate the DFA states using the subset construction scheme [14]. The obtained DFA states provide us with all valid NFA active state combinations. The complexity of this step depends on the degree of state explosion incurred by the regular expression set. In the worst case, an NFA with  $N_N$  states can lead to  $2^{N_N}$  DFA states. Note that although the construction of a TFA requires obtaining all DFA states via subset construction, it does not require the generation of a complete DFA (even temporarily) since no DFA transition is computed or stored. In our experiments, the total memory usage during this procedure is only 1% of that for compiling the complete DFA which, however, is required by some other schemes, such as D<sup>2</sup>FA [6] and CD<sup>2</sup>FA [15], as an intermediate data structure.

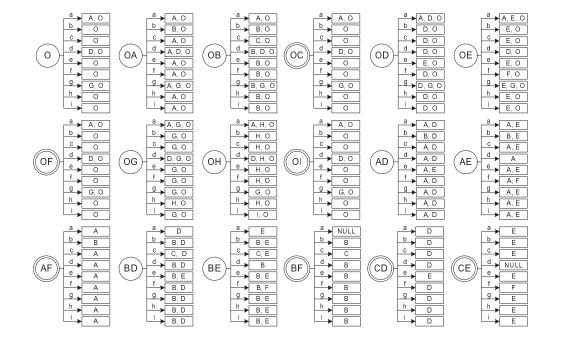
(2) Split each NFA active state combination into up to b subsets, with the objective of minimizing the number of distinct subsets, and generate one TFA state for each distinct subset. After this step, we obtain the TFA state set  $Q_T$  and the set split function SS;

(3) Decide the transition function  $\delta_T$ . Different from traditional automatons, outgoing transitions of TFA states do not point to other TFA states. Instead, they point to a data structure called *state label*, which contains a set of NFA state IDs. Given a TFA state *s*, its state label associated with character "*c*" includes all NFA states that can be reached via character "*c*" from the NFA states associated with TFA state *s*. For instance, consider TFA state "AD" in Figure 3(a); its state label associated with character "*b*" is {B, D}, which can be obtained by running state "A" and "D" using "*b*" in the NFA.

(4) Decide the set of initial states (I) and the set of accept states ( $F_T$ ). Set I includes TFA states split from the initial state of the counterpart DFA (i.e.,  $q_D$ ). Set  $F_T$  includes TFA states associated with at least one NFA accept state.

# C. Operating A TFA

Algorithm 1 describes the operations of a *b*-TFA in each time slot. Consider the TFA in Figure 3 and assume the input



(a) A TFA structure with b=2

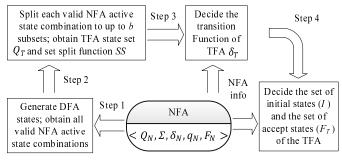


Fig. 4. The flowchart for generating a TFA.

string is "adegf". After reading in the first character "a", the initial active state of TFA "O" returns a state label {A, O}. We use {A, O} to run a set query in the SST table, which returns the memory address of the next active TFA state (i.e., &OA in this case). With the next character "d", active state "OA" will return state label {A, D, O}. After searching the SST table using {A, D, O}, we get two active states (&O and &AD). With the third input character "e", active state "O" returns state label {O} and active state "AD" returns state label {A, E}. The union of the two labels (i.e., {A, E, O}) is sent to the SST table to find the next active states (&OA and &AE). The above procedure repeats in every time slot until the entire input string is scanned.

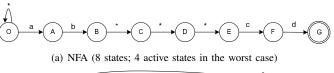
It should be noted that the scheme of TFA is essentially different from the DFA grouping scheme proposed in [5]. The DFA grouping scheme cannot be applied or performs badly under certain circumstances, such as the situations in which the rule set has only one regular expression, or has multiple regular expressions but one of them is extremely complex. Consider the NFA and DFA shown in Figure 5(a) and 5(b), which represent a single regular expression  $.*ab.{3}cd$  used in [8]. Apparently the DFA grouping scheme cannot be used in this single-rule case; however, the TFA can still be adopted to reduce the memory cost. Consider the 2-TFA with only 9 states shown in Figure 5(c); we can always use (at most) two 2-TFA states to exactly cover a valid combination of NFA active states. This example also shows the efficiency of TFAs when handling regular expressions with repetitions (i.e., counting).

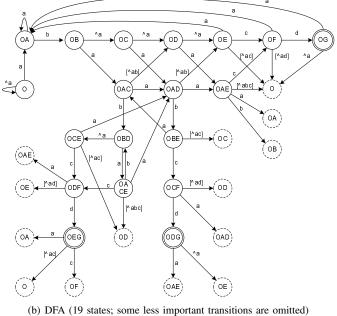
In Section V and VI, we will address two critical issues in

Algorithm 1	Operations	of l	b-TFA	in	each	time	slot
-------------	------------	------	-------	----	------	------	------

1: Input:  $s \ (s \le b); \ \triangleright$  no. of active states in current time slot 2: ▷ current active states 3:  $A[j](j=1,\ldots,s);$ 4: ▷ input character c: 5: **Output:**  $s' \ (s' \le b); \quad \triangleright \text{ no. of active states in next time slot}$ 6:  $A'[j](j = 1, ..., s'); \triangleright$  active states in next time slot 7: 8: T = NULL;9: 10: for (j = 1, ..., s) do 11:  $T = T \cup$  state label on state A[j] labeled with c; 12: end for

13: use T to access SST table, returning s' and  $A'\left[j\right](j=1,...,s')$ 





(c) 2-TFA (9 states; at most 2 active states; only states of TFA are shown for simplicity)

OD

OE

AC

G

Fig. 5. Automatons representing a single regular expression .\*ab.{3}cd.

the implementation of a TFA: (1) How to split the NFA active state combinations to obtain a small TFA; (2) How to store TFA and SST table efficiently to facilitate the TFA operation.

#### V. SPLITTING NFA ACTIVE STATE COMBINATIONS

#### A. Set Split Problem (SSP)

в

А

ос

0

The SSP problem is to split each NFA active state combination into up to b non-empty subsets (overlaps among the subsets are allowed). To get a small TFA, the number of distinct subsets after the set split should be minimized. The SSP problem can also be rephrased to a special set covering problem; i.e., to find a minimal number of subsets from the NFA state set, so that for any valid NFA active state combination, we can always find up to b subsets to exactly cover it. We denote the SSP problem with a bound factor of b as b-SSP problem, and formalize it in Table II based on notations in Table I.

 TABLE I

 NOTATIONS USED IN b-SSP PROBLEM

Notations	Descriptions
$Q_N$	The set of all NFA states
ND	The number of different combinations of NFA active states
	(i.e., the number of states in the corresponding DFA,
	$N_D =  Q_D $
$S_i$	The <i>i</i> -th combination of NFA active states $(i =$
	$1,, N_D$ )
$S_{i,j}$	The <i>j</i> -th subset split from $S_i$ $(j = 1,, b)$
$Q_T$	The union of $S_{i,j}$ $(i = 1,, N_D; j = 1,, b)$

We have proved that the *b*-SSP problem is an NP-hard problem for any b > 1 (please refer to the proof in the Appendix). Thus, no optimal solution can be found to solve it in polynomial time. We present here a heuristic algorithm to solve the *b*-SSP problem.

#### B. A Heuristic Algorithm for SSP Problem

To simplify the problem, we add another constraint (called *isolation constraint*) on the model of the *b*-SSP problem, which is shown in (5).

$$S_{i,j} \bigcap S_{i,k} = \emptyset \quad (\forall j \neq k; \ i = 1, ..., N_D)$$
(5)

The isolation constraint requires that there be no overlap between the subsets split from the same NFA active state combination.

#### B.1 2-SSP Problem

We first consider the *b*-SSP problem with b = 2. Later we consider more general situations with b > 2.

Let  $v_i$  be the number of states in the *i*-th NFA active state combination. The number of different ways to split the combination (denoted as  $F_i$ ) under the 2-SSP problem can be expressed as follows.

$$F_i = 2^{v_i - 1} {(6)}$$

Since there are  $N_D$  different NFA active state combinations, the number of possibilities to split these state combinations is  $\prod_{i=1}^{N_D} F_i$ . Obviously, the problem space is too large if we go through every possibility of the split. To design a practical algorithm, we need to reduce the problem space.

Given an NFA active state combination with v states, we consider only two kinds of special splits:

- (1) No split at all (i.e., one subset is empty);
- (2) Splits that divide the combination into two subsets whose sizes are 1 and v 1, respectively.

This way, we can reduce the value of  $F_i$  from that given in (6) to  $v_i + 1$ . The reason to use the second special split is that, after analyzing the NFA active state combinations of many rule sets, we find many combinations of NFA active states differ from each other in only one NFA state. For instance, combination {A, B, C, D}, {A, B, C, E}, {A, B, C, F}, and {A, B, C, G} differ from each other only in the last state. Splitting {A, B, C} out from these combinations gives us five subsets, i.e., {A, B, C}, {D}, {E}, {F}, {G}. It is very likely that the four single-element subsets are already (or will be) used in other splits, so the four splits produce only one distinct subset {A, B, C}, resulting in a high reusability of subsets.

TABLE II Set Split Problem

Subject to.		
$\bigcup S$	$i_{i,j} = S_i; (i = 1,, N_D; j = 1,, b)$	(2)
$Q_T = \{$	$S_{i,j} \mid i = 1,, N_D; j = 1,, b\} - \{\emptyset\}$	(3)
Objective.	Minimize: $ Q_T $	(4)

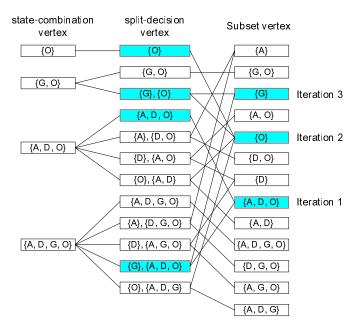


Fig. 6. Tripartite graph used in the heuristic algorithm.

The main data structure used in the heuristic algorithm is a tripartite graph, as shown in Figure 6. Each vertex in the left partition (called a *state-combination vertex*) corresponds to an NFA active state combination. Each vertex in the middle partition (called a *split-decision vertex*) denotes a valid split decision for one of the combinations. Each vertex in the right partition (called a *subset vertex*) corresponds to a unique subset obtained via the splits in the middle partition. Each split-decision vertex is connected with its associated statecombination vertex, as well as the subset vertices that can be obtained from the split decision.

The heuristic algorithm runs in multiple iterations to find a set of subsets  $(Q_T)$  which satisfies the constraints in the 2-SSP problem. Initially,  $Q_T$  is empty. In each iteration, the algorithm starts with the subset vertices in the right partition, from which we select the largest-degree subset (the number of connected edges) among the subsets whose sizes and degrees are both larger than 1<sup>4</sup>. If no such subset is found, we select the largest-degree subset from among all the subsets. The selected subset will be put into  $Q_T$ . The split-decision vertices connected with the selected subset vertices will also be selected and stored in the SST table. Then we remove those vertices that will not be used from the tripartite graph. We remove (1) the state-combination vertices (including their edges) connected with the selected split-decision vertices; (2) the split-decision vertices (including their left and right edges) previously connected with the just-removed state-combination vertices; (3) the subset vertices whose degrees are zero. Now one iteration is finished. The iteration repeats until all subset vertices are removed or selected.

Consider the example in Figure 6. In the first iteration, we select subset vertex  $\{A, D, O\}$ , and split-decision vertices

<sup>&</sup>lt;sup>4</sup>The reason that we first consider subsets with sizes larger than 1 is that the second special split tends to generate many large-degree single-element subset candidates (such as  $\{O\}$  in the figure). Selecting these subsets in the very beginning will cause many unwanted splits.

 $\{\underline{A}, D, O\}$  and  $\{\underline{G}\}$ ,  $\{\underline{A}, D, O\}$ . In the second iteration, we select the subset vertex  $\{\underline{O}\}$ , and split-decision vertices  $\{\underline{O}\}$  and  $\{\underline{G}\}$ ,  $\{O\}$ . In the third iteration, we select the subset vertex  $\{\underline{G}\}$ . After running the heuristic algorithm, four NFA active combinations are split into three TFA states.

#### B.2 b-SSP Problem

Given an arbitrary algorithm solving the 2-SSP problem, we can easily expand it to support the *b*-SSP problem if *b* is equal to the power of 2. We just need to run the algorithm of the 2-SSP problem recursively for  $\log_2 b$  times, each time using the output of the previous run as the input.

To solve the *b*-SSP problem when *b* is an arbitrary integer, we can run the proposed heuristic algorithm for b - 1 times, each time using the output of the previous run as the input.

# VI. STATE ENCODING

A challenging issue involved in the TFA implementation is the storage of state labels, since different state labels include different numbers of NFA state IDs. A simple scheme is to implement each state label as an array, including all associated NFA state IDs. However, this scheme causes two problems: (1) high storage cost and (2) TFA operation overhead.

Let us recall the operations of a TFA in one time slot (Algorithm 1). After examining the outgoing transitions of current active states, the TFA returns up to b state labels, each containing a set of NFA state IDs. A union operation is required on these ID sets, and the result is sent to the SST table to search for the next active states. To achieve a constant lookup performance, we implement the SST table as a perfect hash table as proposed in [24] and [25]. However, the perfect hashing implementation requires the set union operation to return a deterministic and unique representation (i.e., hash key) for each valid combination of NFA active states. If we were to implement each state label as an array, two complicated operations would be required after the set union operation:

- Redundancy elimination. Consider two state labels {D, O} and {G, O}. To get their union, we have to identify and remove one redundant state "O".
- Sorting. The unions of different state labels could result in different representations for the same NFA active state combination (for example, {O, D, G} and {D, G, O} are logically the same). We have to sort state IDs in the result set before performing the table lookup.

To overcome the problems above, we propose an efficient state encoding scheme.

# A. State Encoding Problem

The main idea of state encoding is to assign a bit vector to each NFA state (as its ID), so that the union operation on multiple NFA states can be replaced by a simple bitwise OR operation. As a result, the redundancy elimination and sorting operations are no longer needed to get a deterministic representation for each NFA active state combination. Furthermore, with state encoding, each state label in the TFA structure no longer needs to store the IDs of all associated NFA states (refer to Figure 3(a)). Instead, only the result of the bitwise OR operation on these NFA state IDs needs to be stored. Therefore, all state labels have the same length in bits.

In order to operate the TFA correctly, only one constraint needs to be satisfied in the state encoding; that is, *the bit vector associated with each valid combination of NFA active states* (*i.e., each DFA state*) *must be unique*. We call it *uniqueness constraint*.

The state encoding problem (SEP) can be formally described as follows: find a way to assign each NFA state a bit vector, so that (1) the uniqueness constraint is satisfied; (2) the number of bits used in the bit vector is minimized.

#### B. State Encoding Algorithm

The state encoding problem is an NP-hard problem. Here we propose a practical algorithm to solve it.

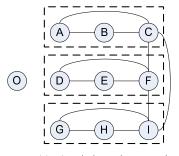
Given an NFA, we first construct an independent graph, where each node corresponds to one NFA state. Two nodes in the independent graph are connected by an edge, if and only if their associated NFA states are never active together (i.e., never together in one NFA active state combination). An example of the independent graph is shown in Figure 7(a), which corresponds to the NFA in Figure 1(a). The independent graph can be directly obtained in the first step of TFA construction: when generating the DFA states using the subset construction scheme [14], as depicted in Figure 4. Every time when we generate a DFA state using the subset construction scheme, we are able to tell whether two NFA states are active together corresponding to this DFA state or not. For instance, when we generate a DFA state, say OBG, in Figure 1(b), we should be able to tell that NFA states O, B, G are active together corresponding to DFA state OBG. After generating all DFA states, we should be able to get a dependent graph, where each node represents a NFA state and each edge indicates that the connected two NFA states occur (i.e., being active together) at least once in a DFA state. Then by simply inversing the dependent graph, we can get the required independent graph.

We then divide nodes of the independent graph into a small number of maximal cliques, and denote these cliques by  $C_j$  (j = 1, ..., n), where n is the number of cliques. Let  $m_j$  be the size of clique  $C_j$ . The state encoding scheme requires  $\sum_{j=1}^{n} \lceil \log_2(m_j + 1) \rceil$  bits in total for the encoding. Nodes in clique  $C_1$  are encoded contiguously using bit position No.1 to No.  $\lceil \log_2(m_1 + 1) \rceil$ , with other bit positions left as "0". Nodes in clique  $C_k$  (k > 1) are encoded using bit position No.  $\sum_{j=1}^{k-1} \lceil \log_2(m_j + 1) \rceil + 1$  to No.  $\sum_{j=1}^{k} \lceil \log_2(m_j + 1) \rceil$ , with other bit positions left as "0". Consider the independent graph in Figure 7(a), from which we can get four cliques. The first clique includes "A", "B", and "C", which are encoded consecutively using the first two bit positions (as shown in the state encoding map in Figure 7(b)).

It is easy to prove that with the state encoding scheme, each valid combination of NFA active states will have a unique bit vector, because the NFA states in each valid combination are always from different cliques and use different bit positions.

TABLE IV STATE NUMBERS OF AUTOMATONS UNDER DIFFERENT REGULAR EXPRESSION SETS

Rule set	e set NFA DFA Hybrid-FA			DFA Grouping		TFA						
Tule set				b=2	b=3	b=4	b=2	% reduction	b=3	% reduction	b=4	% reduction
Snort 1	385	369870	7167	63637	10166	1857	6563	98.23%	1461	99.60%	871	99.76%
Snort 2	191	100697	7955	16851	8396	3816	5986	94.06%	805	99.20%	284	99.72%
Snort 3	38	363154	63443	363154	363154	363154	62133	82.89%	22418	93.83%	14668	95.96%
Snort 4	417	158060	11714	11459	6181	2434	10846	93.14%	3005	98.10%	1597	98.99%
Bro	567	407677	22305	148665	6890	2316	16576	95.93%	3685	99.10%	2000	99.51%



00000 A B: 0 0 0 0 0 0 C 1 0 0 0 0 0 D: 0 0 0 1000 0 0 1 0 0 0 0 E: 0 0 1 1 0 0 0 F G 0 0 0 0 0 1 0 0 0 0 0 1 0 0 H: 0 0 0 0 1 1 0 000001 0:

(a) An independent graph corresponding to the NFA in Figure 1 (a).

(b) State encoding map based on cliques in (a).

Fig. 7. Independent graph and state encoding map.

# VII. PERFORMANCE EVALUATION

# A. Evaluation Settings

The evaluation is made based on regular expression rule sets from Snort and Bro. Each rule in Snort includes header pattern, exact strings, and regular expressions. Since header pattern matching and exact string matching have been wellstudied and are not the focus of this paper, we consider only regular expressions in the evaluation process. We cluster regular expressions with common header patterns together and evaluate them using traffic traces collected from our campus network at different time points. The regular expression rule sets selected include all major characteristics appearing in Snort and Bro rules, such as infinite repetitions, finite repetitions, and character ranges. Table III gives the statistics of these regular expression sets.

We compare the TFA against the NFA, DFA and two other state-of-the-art DFA-based solutions: DFA grouping [5] and Hybrid-FA [8]. The latter two schemes are selected because they have a similar motivation to ours, i.e., to trade off the storage with the number of active states. For a given regular expression set, the DFA grouping scheme [5] minimizes the

 TABLE III

 BASIC STATISTICS OF REGULAR EXPRESSION RULE SETS

Rule set	# of RegEx	Avg. ASCII length of RegEx	% RegEx having infinite repetitions, e.g., "*", "+"	% RegEx having finite repetitions, e.g., {n,m},"?"	% RegEx having char ranges, e.g., \s,".", [a-z]
Snort 1	17	38.6	88.2%	41.2%	94.1%
Snort 2	12	16.7	16.7%	83.3%	100.0%
Snort 3	1	19.0	100.0%	100.0%	100.0%
Snort 4	34	29.7	32.4%	58.8%	82.4%
Bro	63	14.6	11.1%	12.7%	23.8%

number of DFAs subject to either of the two storage constraints: (1) the size of each DFA is limited and (2) the total size of DFAs is limited. Since our objective is different, which is to minimize the storage with a bounded number of active states, we need to modify the DFA grouping scheme for a straight comparison. A bound factor b implies that we can use b DFAs in the DFA grouping scheme. The modified DFA grouping scheme still uses the interaction concept proposed in [5]. It first creates b empty groups, and then recursively inserts each regular expression into the group that has the least interactions with the regular expression undergoing. Hybrid-FA is another effective solution to achieve a tradeoff between storage complexity and run-time complexity. To get a better worst-case run-time performance, we generate Hybrid-FA with tail-DFAs. To make fair comparisons, we use states rather than counters to implement the finite repetitions in all automatons in the evaluation.

# B. Storage Complexity

The storage of a TFA consists of two parts: a TFA structure and an SST table.

# B.1 Storage of SST Table

The storage of the SST table is a fixed expense for a TFA. Consider that (1) the number of entries in the SST table is equal to the DFA state number, and (2) each entry in the SST table stores the addresses of b states. The SST table for a b-TFA requires b/256 times the memory required by the corresponding DFA. Normally, a practical TFA takes b between 2 and 4, so the storage cost of the SST table is about  $0.78\% \sim 1.5\%$  that of the DFA. Perfect hashing implementation of the SST table provides O(1) run-time performance but requires extra entries to facilitate the table construction. In this paper, we use cuckoo hashing [24] to build the SST table. In our experiment, an SST table with millions of entries can be constructed with a load factor of 0.7 in 5 minutes. The storage cost of the SST table including the extra entries for perfect hashing implementation is about  $1.11\% \sim 2.14\%$  of that required by a DFA. This result is based on ASCII encoding, where the size of input symbol set  $|\Sigma|$  is 256. If we consider other larger input symbol sets, such as UTF-8 or UTF-16, which support up to millions of symbols, the space cost of SST will be even less significant. Furthermore, some DPI methods based on ASCII encoding use a scanning stride larger than one (i.e., scanning more than one input symbols each time). This will cause an effect similar

TABLE V STATISTICS OF STATE ENCODINGS IN THE TFA

		b=2		b=	3	b=4	
	#ofbits		% of		% of		% of
rule set	in state	# of state	distinct	# of state	distinct	# of state	distinct
	encoding	labels	state	labels	state	labels	state
			labels		labels		labels
Snort 1	40	1680128	0.87%	374016	0.98%	222976	0.88%
Snort 2	28	1532416	0.52%	206080	0.56%	72704	0.68%
Snort 3	38	15906048	0.48%	5739008	0.42%	3755008	0.46%
Snort 4	61	2776576	0.69%	769280	0.60%	408832	0.63%
Bro	55	4243456	0.64%	943360	0.77%	512000	0.90%

to the large input symbol set, where the space cost of SST becomes less significant.

#### B.2 Storage of TFA Structure

The memory cost of a TFA structure depends on two factors: (1) TFA state number and (2) the number of bits used in state encoding.

Table IV compares the state numbers of finite automatons under different rule sets. We can see that with only two active states, a TFA can significantly reduce the number of states required by a DFA (the reduction rates are more than 93% under all tested rule sets except "Snort 3", which will be discussed shortly.) The reduction rates are above 98% when three active states are used. Allowing more active states leads to an even higher reduction. The DFA grouping scheme is inferior to the TFA scheme when b is small. When b is large enough, the difference between them becomes smaller. "Snort 3" is a single-rule rule set including nested character ranges and finite repetitions. From Table IV we can see that the DFA grouping scheme cannot handle those cases with single complex regular expression, whereas the TFA scheme is still in effect and can achieve a 96% reduction when b = 4.

To make a straight comparison, the head-DFA and tail-DFAs of the Hybrid-FA are adjusted so that their total state number is close to the state number of the TFA with b = 2. (A smaller Hybrid-FA can always be achieved by moving more states from the head-DFA to tail-DFAs, a process, however, that results in an even worse run-time performance.) Later we will compare the run-time performance between the Hybrid-FA and the TFA under this condition.

Table V shows the number of bits required by state encoding of the TFA under different rule sets. It also shows the number of state labels and the fraction of distinct state labels under each tested rule set. It can be seen that the fraction of unique state labels is very low (lower than 1% under all tested rule sets). This characteristic can be potentially exploited to further compress the memory usage.

Table VI compares the overall memory usages of different finite automatons. When calculating the memory usage of the TFA, we consider the cost of both the SST table and TFA structure. The TFA scheme with b = 2 can achieve a memory reduction between 93.27% and 97.44% in all tested rule sets, except the single-rule set "Snort 3", under which a reduction rate of 82.11% is achieved.

TABLE VI Memory costs of automatons

rule set	DFA (MB)	Hybrid-FA	DFA Grouping	TF/	A (b=2)
Tule Set		(MB)	1B) (b=2)(MB)		% reduction
Snort 1	379	7.34	65.16	9.68	97.44%
Snort 2	103	8.15	17.26	6.94	93.27%
Snort 3	372	64.97	371.87	66.53	82.11%
Snort 4	162	12.00	11.73	9.59	94.07%
Bro	417	22.84	152.23	20.24	95.15%

TABLE VII NUMBER OF ACTIVE STATES

rule set		NFA		Hybrid-F	TFA (b=2)	
Tule Set	Max	Worst case	Max	#tail-FA	Worst case	Worst case
Snort 1	10	16	3	12	13+	2
Snort 2	3	8	3	5	6+	2
Snort 3	3	33	3	1	2+	2
Snort 4	8	18	5	15	16+	2
Bro	21	25	10	22	23+	2

# C. Memory Bandwidth Requirement

The memory bandwidth requirement (or the run-time speed) of an automaton can be expressed by the number of states which are activated during the processing of a character. Unlike the NFA and Hybrid-FA, a TFA can have the number of active states strictly bounded by the bound factor *b*; therefore it has a deterministic matching speed independent of the regular expression rule sets and traffic patterns.

Table VII shows the numbers of active states of the NFA, Hybrid-FA and TFA. The third and sixth columns give the theoretically worst case scenarios for the NFA and Hybrid-FA respectively. Note that when a tail-DFA of the Hybrid-FA includes a finite repetition, it might be triggered multiple times, resulting in a large number of active states in the tail-DFA [8]. It is discussed in [8] that with the use of a counter, the number of active states in such a tail-DFA can be limited to 2. In Table VII, we use a conservative way to calculate the worst-case active state number for the Hybrid-FA. We count only one active state for each tail-DFA. So the number of active state in the Hybrid-FA in the worst case is equal to the number of tail-DFAs plus one. The second and fourth columns of Table VII give the maximal numbers of active states we observed in the inspection of two million packets using the NFA and Hybrid-FA.

# VIII. CONCLUSION

Given the fact that the rule sets of many NIDSes (such as Snort and Bro) are available online, cyber criminals can easily construct a worst-case traffic pattern after carefully studying the rule sets for ways to slow down the NIDS and let malicious traffic escape from the inspection. Therefore, the worst-case run-time performance is critical to an NIDS if it is to survive under the network attack. In this paper, we have proposed a new finite automaton representation, TFA, for regular expression matching. It combines the strengths of both NFAs and DFAs by allowing a small bounded number of active states during the matching processing, so that the worstcase performance of a TFA is guaranteed. The construction of the TFA relies on an efficient solution to the Set Split Problem (SSP). Although an efficient heuristic algorithm has been proposed for the SSP problem in this paper, we believe that better solutions for the problem are still available, which may further bring down the storage cost of a TFA. Moreover, other related ideas, such as using counters to represent finite repetitions in regular expressions, can also be applied to the TFA to achieve even more compact finite automatons.

#### REFERENCES

- S. Wu and U. Manber, "A fast algorithm for multi-pattern searching," Dept. of Computer Science, University of Arizona, Tech. Rep., 1994.
- [2] A. V. Aho and M. J. Corasick, "Efficient string matching: An aid to bibliographic search." *Commun. of the ACM*, vol. 18, no. 6, pp. 333– 340, 1975.
- [3] S. Dharmapurikar and J. W. Lockwood, "Fast and scalable pattern matching for network intrusion detection systems." *IEEE J SEL AREA COMM*, vol. 24, no. 10, pp. 1781–1792, 2006.
- [4] N. Tuck, T. Sherwood, B. Calder, and G. Varghese, "Deterministic memory-efficient string matching algorithms for intrusion detection." in *Proc. of IEEE INFOCOM*, 2004.
- [5] F. Yu, Z. Chen, Y. Diao, T. V. Lakshman, and R. H. Katz, "Fast and memory-efficient regular expression matching for deep packet inspection," in *Proc. of ACM/IEEE ANCS*, 2006.
- [6] S. Kumar, S. Dharmapurikar, F. Yu, P. Crowley, and J. Turner, "Algorithms to accelerate multiple regular expressions matching for deep packet inspection," in *Proc. of ACM SIGCOMM*, 2007.
- [7] R. Smith, C. Estan, and S. Jha, "XFA: Faster signature matching with extended automata," in *IEEE Symposium on Security and Privacy*, 2008.
- [8] M. Becchi and P. Crowley, "A hybrid finite automaton for practical deep packet inspection," in *Proc. of ACM CoNEXT*, 2007.
- [9] P.-C. Lin, Y.-D. Lin, Y.-C. Lai, and T.-H. Lee, "Using string matching for deep packet inspection," *IEEE Computer*, vol. 41, no. 4, pp. 23–28, 2008.
- [10] R. Sommer and V. Paxson, "Enhancing byte-level network intrusion detection signatures with context," in *Proc. of the ACM Conference on Computer and Communications Security (CCS)*, 2003.
- [11] A free lightweight network intrusion detection system for UNIX and Windows. [Online]. Available: http://www.snort.org
- [12] Bro Intrusion Detection System. [Online]. Available: http://www.broids.org
- [13] Cisco IPS Deployment Guide. [Online]. Available: http://www.cisco.com
- [14] J. E. Hopcroft, R. Motwani, and J. D. Ullman, Introduction to automata theory, languages, and computation - international edition (2. ed). Addison-Wesley, 2003.
- [15] S. Kumar, J. Turner, and J. Williams, "Advanced algorithms for fast and scalable deep packet inspection," in *Proc. of ACM/IEEE ANCS*, 2006.
- [16] Y. Yang and V. Prasanna, "Space-time tradeoff in regular expression matching with semi-deterministic finite automata," in *Proc. of IEEE INFOCOM*, 2011, pp. 1853–1861.
- [17] Y. Xu, J. Jiang, Y. Song, T. Jiang, and H. J. Chao, "i-dfa: A novel deterministic finite automaton without state explosion," in *Technical report, Polytechnic Institute of New York University*, 2010.
- [18] N. Laptev and C. Zaniolo, "Optimization of massive pattern queries by dynamic configuration morphing," in *Proc. of IEEE 28th International Conference on Data Engineering (ICDE)*, 2012, pp. 917–928.
- [19] M. Becchi and P. Crowley, "An improved algorithm to accelerate regular expression evaluation," in *Proc. of ACM/IEEE ANCS*, 2007.
- [20] J. Jiang, Y. Xu, T. Pan, Y. Tang, and B. Liu, "Pattern-based dfa for memory-efficient and scalable multiple regular expression matching," in *Proc. of IEEE ICC*, may 2010, pp. 1 –5.
- [21] R. Smith, C. Estan, S. Jha, and S. Kong, "Deflating the big bang: fast and scalable deep packet inspection with extended finite automata," in *Proc. of ACM SIGCOMM*, 2008.
- [22] M. Becchi and S. Cadambi, "Memory-efficient regular expression search using state merging," in *Proc. of IEEE INFOCOM*, may 2007, pp. 1064 –1072.
- [23] S. Kumar, B. Chandrasekaran, J. Turner, and G. Varghese, "Curing regular expressions matching algorithms from insomnia, amnesia, and acalculia," in *Proc. of ACM/IEEE ANCS*, 2007.
- [24] R. Pagh and F. F. Rodler, "Cuckoo hashing," in *Journal of Algorithms*, 2001, p. 2004.

# APPENDIX

# A. Complexity of b-SSP Problem

We have proved that the *b*-SSP problem is an NP-hard problem for any b > 1. Due to space limitations, we briefly describe the reductions that prove *b*-SSP problem is in NPhard. The reductions are done to the decision problem of *b*-SSP called *b*-SSPD, and prove that *b*-SSPD is in NP-complete. Given a threshold *r* of the objective (i.e.,  $|Q_T|$ ), *b*-SSPD asks if  $|Q_T|$  can be less than *r*. We only provide the construction and reductions. The correctness proof is ignored due to space limitations.

First of all, *b*-SSPD is in NP, because any solution of *b*-SSPD which is the partition on each  $S_i$  can be verified whether  $|Q_T| < r$  in polynomial time.

Second, we prove that *b*-SSPD is in NP-complete when *b*=2 by reducing 3SAT to 2-SSPD. For a 3SAT instance of *n* variables and *m* clauses, we create 3n + 7m + 2 sets as follows. For any variable $x_j$ , create three sets  $\{x_j, T, F\}$ ,  $\{\overline{x_j}, T, F\}$ ,  $\{x_j, \overline{x_j}, T, F\}$ . For any clause  $C_k = l_{k,1} \lor l_{k,2} \lor l_{k,3}$ , create seven sets:  $\{u_{k,1}\}, \{u_{k,2}\}, \{u_{k,3}\}, \{C_k, l_{k,1}, l_{k,2}, u_{k,3}\}, \{C_k, l_{k,1}, l_{k,3}, u_{k,2}\}, \{C_k, l_{k,2}, l_{k,3}, u_{k,1}\},$ 

 $\{C_k, l_{k,1}, l_{k,2}, l_{k,3}, T\}$ . The reduction statement is following. There is a solution (i.e., a satisfying assignment) to the 3SAT instance, if and only if there is a solution to the corresponding 2-SSPD instance with 2 + 2n + 6m subsets.

With 2-SSPD in NP-complete, we only need to recursively reduce b-SSPD to (b-1)-SSPD and finally to 2-SSPD, proving that b-SSPD is in NP-complete for any b > 1. Consider a (b-1)-SSPD instance of n sets  $\{S_1\}, ..., \{S_n\}$  and decide whether any partition can produce no more than m distinct subsets. We create the b-SSPD instance by appending n different elements (which do not appear in any set of (b-1)-SSPD instance) to each  $\{S_i\}$ , creating n sets. The n elements appended to  $\{S_i\}$ will be used to create  $\binom{n}{2}$  2-element sets. In sum, a b-SSPD instance of  $n^2 + n\binom{n}{2}$  sets is created for (b-1)-SS instance of n sets. The reduction is following. There is a solution to the (b-1)-SSPD instance with m distinct subsets if and only if there is a solution to the corresponding b-SSPD instance with  $m + n^2$  subsets, where  $m \le n$ .

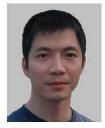


Yang Xu (S'05-M'07) is a Research Associate Professor in the Department of Electrical & Computer Engineering in New York University Polytechnic School of Engineering, where his research interests include Software-Defined Networks, Data Center Networks, Network on Chip, and High Speed Network Security. From 2007-2008, he was a Visiting Assistant Professor in NYU-Poly. Prior to that, he completed a Ph.D. in Computer Science and Technology from Tsinghua University, China in 2007. He received the Master of Science degree in Computer

Science and Technology from Tsinghua University in 2003 and Bachelor of Engineering degree from Beijing University of Posts and Telecommunications in 2001.



Junchen Jiang is a Ph.D student in the Computer Science department at Carnegie Mellon University, where he is advised by Hui Zhang. He received his Bachelor degree in engineering from Computer Science and Technology Department, Tsinghua University, Beijing. His research interests are in computer networks, with an emphasis on Internet video.



**Rihua Wei** (M'12) received his B.S. degree in Telecom Engineering from Beijing University of Posts and Telecoms, Beijing, China, in 1999; his M.S. degrees in electrical and electronics engineering from Tsinghua University, Beijing, China, in 2003, and from Polytechnic Institute of New York University, New York, USA, in 2011. He is currently with Tensorcom Inc., Carlsbad, CA, USA. His areas of interest include networking, ASIC and Systemon-Chip.



Yang Song received the B.S. degree in mathematics from Zhejiang University in Hangzhou, People's Republic of China in 2008, and the M.S. degree in computer science from Polytechnic Institute of New York University in 2011. Currently, he is a PhD candidate at the University of Utah, Salt Lake City. His research interests are in computer-aided design, computer graphics and geometric modeling.



**H. Jonathan Chao** (M'83-F'01) is Department Head and Professor of Electrical and Computer Engineering at New York University Polytechnic School Of Engineering, Brooklyn, NY, where he joined in January 1992. He has been doing research in the areas of network designs in data centers, terabit switches/routers, network security, network on the chip, and biomedical devices. He holds 45 patents and has published over 200 journal and conference papers. He has also served as a consultant for various companies, such as Huawei, Lucent, NEC,

and Telcordia. During 2000-2001, he was Co-Founder and CTO of Coree Networks, NJ, where he led a team to implement a multi-terabit MPLS (Multi-Protocol Label Switching) switch router with carrier-class reliability. From 1985 to 1992, he was a Member of Technical Staff at Telcordia. He received the Telcordia Excellence Award in 1987. He is a Fellow of the IEEE for his contributions to the architecture and application of VLSI circuits in high-speed packet networks. He coauthored three networking books, Broadband Packet Switching Technologies, Quality of Service Control in High-Speed Networks, and High-Performance Switches and Routers, all published by Wiley. He received his PhD in EE from The Ohio State University in 1985.